

Customer No.: 31561  
Docket No.: 13353-US-PA  
Application No.: 10/709,823

**In the specification:**

Please amend paragraph [0012] as below:

[0012] Preferably, the network interface control unit comprises a programmable interface controller and a transistor-transistor logic (hereinafter, referred as TTL)/differential level converting interface. Wherein, the TTL/differential level converting interface is used to convert the type of the real-time data from TTL to differential or in reverse, and to cache the real-time data. In addition, the programmable interface controller comprises a storage apparatus and a sequencer. Wherein, the storage apparatus stores a microcode internally, and the microcode is used to control the operation of the programmable interface controller. The sequencer is coupled to the storage apparatus for running the microcode instructions and adjusting the running order based on an external condition. The programmable interface controller further comprises a condition selector and an event/interrupt handler. Wherein, the condition selector is coupled to the sequencer for caching the external condition, and the external condition is then provided to the sequencer for it to make decision. The event/interrupt handler is coupled to the storage apparatus for processing an interrupt signal or handling an event. In addition, the programmable interface controller further comprises a processor and a parity bit generating/checking apparatus. Wherein, the processor is coupled to the storage apparatus for running the microcode instructions. The parity bit generating/checking apparatus generates a parity bit according to the real-time data provided by the programmable interface controller, and checks the parity bit of the real-time data provided by the programmable interface controller.